Journal of Semiconductors

Hao Mingli and Shi Yin

Volume 31 Number 1 January 2010

	SEMICONDUCTOR PHYSICS
012001	Calculation of band edge levels of strained $Si/(111)Si_{1-x}Ge_x$ (3 pages) Song Jianjun, Zhang Heming, Hu Huiyong, Dai Xianying, and Xuan Rongxi
012002	Influence of gold particle size on melting temperature of VLS grown silicon nanowire (5 pages) Jiang Yanfeng and Zhang Yamin
012003	Optimization of inductively coupled plasma etching for low nanometer scale air-hole arrays in two- dimensional GaAs-based photonic crystals (5 pages) Peng Yinsheng, Ye Xiaoling, Xu Bo, Jin Peng, Niu Jiebin, Jia Rui, and Wang Zhanguo
	SEMICONDUCTOR MATERIALS
013001	Electronic structures of an (8, 0) boron nitride/carbon nanotube heterojunction (3 pages) Liu Hongxia, Zhang Heming, Song Jiuxu, and Zhang Zhiyong
013002	Suppression of extension of the photo-sensitive area for a planar-type front-illuminated InGaAs detector by the LBIC technique (5 pages) Li Yongfu, Tang Hengjing, Li Tao, Zhu Yaoming, Jiang Peilu, Qiao Hui, Li Xue, and Gong Haimei
	SEMICONDUCTOR DEVICES
014001	Large signal RF power transmission characterization of InGaP HBT for RF power amplifiers (5 pages Zhao Lixin, Jin Zhi, and Liu Xinyu
014002	Short channel effect in deep submicron PDSOI nMOSFETs (3 pages) Bu Jianhui, Bi Jinshun, Song Limei, and Han Zhengsheng
014003	Analysis of trigger behavior of high voltage LDMOS under TLP and VFTLP stress (4 pages) Zhu Jing, Qian Qinsong, Sun Weifeng, and Liu Siyang
014004	Design of a high-performance PJFET for the input stage of an integrated operational amplifier (5 pages) Shui Guohua, Tang Zhaohuan, Wang Zhikuan, Ou Hongqi, Yang Yonghui, Liu Yong, and Wang Xueyi
014005	A novel fabrication approach for an athermal arrayed-waveguide grating (3 pages) Zhou Tianhong and Ma Weidong
014006	Optimization of grid design for solar cells (4 pages) Liu Wen, Li Yueqiang, Chen Jianjun, Chen Yanling, Wang Xiaodong, and Yang Fuhua
	SEMICONDUCTOR INTEGRATED CIRCUITS
015001	A 4224 MHz low jitter phase-locked loop in 0.13-μm CMOS technology (5 pages) Chen Hu, Lu Bo, Shao Ke, Xia Lingli, Huang Yumei, and Hong Zhiliang
015002	A high performance 90 nm CMOS SAR ADC with hybrid architecture (7 pages) Tong Xingyuan, Chen Jianming, Zhu Zhangming, and Yang Yintang
015003	An X-band four-way combined GaN solid-state power amplifier (7 pages) Chen Chi, Hao Yue, Feng Hui, Gu Wenping, Li Zhiming, Hu Shigang, and Ma Teng
015004	A 2.4 GHz power amplifier in 0.35 μ m SiGe BiCMOS (4 pages)

015005	A Ka-band low-noise amplifier with a coplanar waveguide (CPW) structure with 0.15-μm GaAs pHEMT technology (4 pages) Wu Chia-Song, Chang Chien-Huang, Liu Hsing-Chung, Lin Tah-Yeong, and Wu Hsien-Ming
015006	Full on-chip and area-efficient CMOS LDO with zero to maximum load stability using adaptive frequency compensation (6 pages) Ma Haifeng and Zhou Feng
015007	Study and analysis of coefficient mismatch in a MASH21 sigma-delta modulator (4 pages) Ge Binjie, Wang Xin'an, Zhang Xing, Feng Xiaoxing, and Wang Qingqin
015008	Low-power variable frequency PFC converters (5 pages) Li Yani, Yang Yintang, and Zhu Zhangming
015009	A high efficiency charge pump circuit for low power applications (5 pages) Feng Peng, Li Yunlong, and Wu Nanjian
015010	A novel precision curvature-compensated bandgap reference (4 pages) Zhou Zekun, Ming Xin, Zhang Bo, and Li Zhaoji