

AlGaIn/GaN HEMTs Power Amplifier MIC with Power Combining at C-Band*

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Abstract: A power amplifier MIC with power combining based on AlGaIn/GaN HEMTs was fabricated and measured. The amplifier consists of four $10 \times 120 \mu\text{m}$ transistors. A Wilkinson splitters and combining were used to divide and combine the power. By biasing the amplifier at $V_{\text{DS}} = 40\text{V}$, $I_{\text{DS}} = 0.9\text{A}$, a maximum CW output power of 41.4 dBm with a maximum power added efficiency (PAE) of 32.54% and a power combine efficiency of 69% was achieved at 5.4 GHz.

Key words: AlGaIn/GaN HEMTs; power combining; MIC; power amplifiers

EEACC: 1350F; 1350H

CLC number: TN325⁺.3

Document code: A

Article ID: 0253-4177(2007)04-0514-04

1 Introduction

With the development of the wireless communications, telecommunications, data communications and aerospace systems, the demand for solid-state power amplifiers has been continually increasing over the last decade. GaAs technology is currently used to realize power amplifiers at high frequencies. However in many applications, GaAs will not meet the future system requirements because of the thermal and electrical limitations of the GaAs transistors. Under such circumstance, the third generation wide band-gap semiconductors with an order of magnitude or higher breakdown voltage along with excellent thermal properties began to emerge.

As the representation of the third generation semiconductors, the GaN high electron mobility transistors (HEMTs) on SiC are very attractive for power applications for microwave and millimeter wave. In recent years there have been several reports on the large-signal performance of AlGaIn/GaN HEMTs as devices with high output

power^[1~3]. So far there have been few reports on microwave integrated circuits based on AlGaIn/GaN transistors^[4~6]. In this paper we report on design, fabrication and performance of a MIC C-band power amplifier.

2 Technology and fabrication

Figure 1 shows the layer structure of the transistor which is an $\text{Al}_{0.2}\text{Ga}_{0.8}\text{N-AIN-GaN}$ multilayer grown on a semi-insulation 4H-SiC substrate. The wafer was grown by MOCVD. An averaged electron mobility of $1250\text{cm}^2/(\text{V} \cdot \text{s})$ and a sheet carrier density of $1.4 \times 10^{13}\text{cm}^{-2}$ were obtained by Hall measurement. The transistor was fabricated using for ion implant mesa isolation and Ti/Al/Ti/Au for ohmic contacts resulting in a contact resistance of $1.2\Omega \cdot \text{mm}$, and Ni/Au for $0.8\mu\text{m}$ gate. A $10 \times 120\mu\text{m}$ transistor delivers 1340mA corresponding to the current density $I_{\text{DSS}} = 1.11\text{A/mm}$. A $120\mu\text{m}$ device demonstrates a maximum transconductance of 240mS/mm and a threshold voltage of $V_{\text{th}} = -5.2\text{V}$. Small signal measurements yield a cutoff frequency of $f_{\text{T}} = 13\text{GHz}$ and a maximum frequency of oscillation of

* Project supported by the State Key Development Program for Basic Research of China (No. 2002CB311903) and the Key Knowledge Innovation Program of the Chinese Academy of Sciences (No. KG CX2-SW-107)

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Received 8 October 2006, revised manuscript received 27 October 2006

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$f_{max} = 18\text{GHz}$.

i-AlGaIn	5nm
n-AlGaIn	10nm
i-AlGaIn	5nm
AlN	1nm
GaN	$3\mu\text{m}$
4H-SiC	

Fig.1 Layer structure for transistor grown by MOCVD

3 Circuit design

Figure 2 shows a picture of the amplifier which is one stage C-band internal match power amplifier consisting of four $10 \times 120\mu\text{m}$ transistors, Wilkinson splitters and Wilkinson combining. The input and output ports are matched to 50Ω . With the increase of the device power, the input impedance of the device decreases rapidly. Thus the input port was difficult to match to 50Ω . However, power combining is a good idea to solve this problem. A Wilkinson combining and dividers were used to combine and split the power and use to transform the impedance of the transistor^[7]. This transformation allows for easier matching of the HEMTs and for less area consumption than the tradition Wilkinson combining. Because of the small signal model is given at the bias point $V_{DS} = 30\text{V}$, $I_{DS} = 0.5\text{A}$, the design is based on the model.

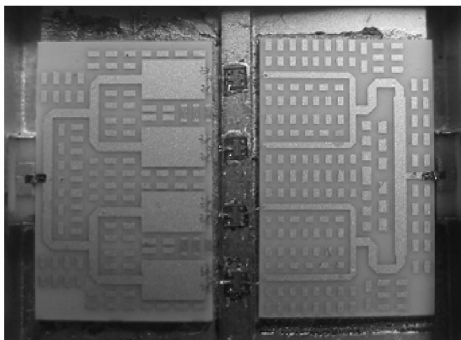


Fig.2 AlGaIn/GaN based C-band MIC consisting of four $10 \times 120\mu\text{m}$ transistors, Wilkinson splitters and Wilkinson combining

4 RF performance

The amplifier was been packaged in a pack-

age, the size of which was $13\text{mm} \times 12.5\text{mm}$. All the measurements have been carried on the test fixture in 50Ω environment (Fig. 3).

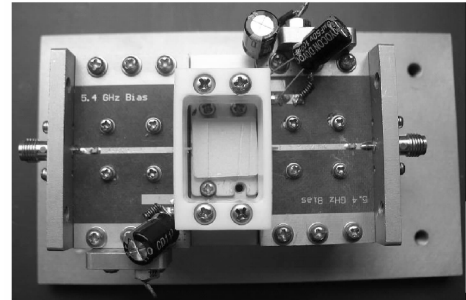


Fig.3 Amplifier test fixture

4.1 Small signal performance

The measured small signal performance of the amplifier at the bias point $V_{DS} = 30\text{V}$, $I_{DS} = 0.5\text{A}$ is shown in Fig. 4. Under small signal conditions the amplifier shows the optimal frequency of around 5GHz, but at the large signal conditions it shifts to 5.4GHz at the bias point $V_{DS} = 40\text{V}$, I_{DS}

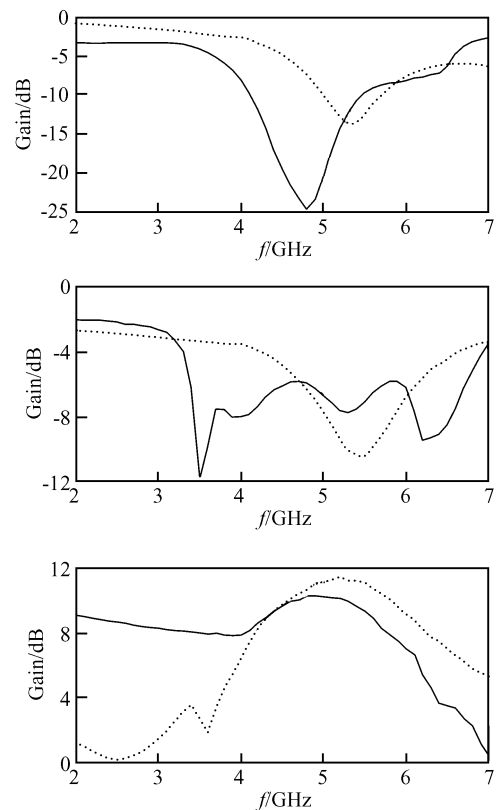


Fig.4 Small signal performance difference between the design (dotted line) and the measure (solid line)

= 0.9A. Figure 4 also shows the difference between the small signal simulation and measurement at the bias point $V_{DS} = 30V$, $I_{DS} = 0.5A$. The difference is caused by the indetermination for the length of the bond wire.

4.2 Large signal performance

Figure 5 shows the power performance of the amplifier at 5.4GHz. By biasing the MIC at $V_{DS} = 40V$, $I_{DS} = 0.9A$, a maximum CW output power of 41.4dBm (13.8W) and a maximum PAE of 32.54% were measured. The output power at 1dB gain compression point is 39.8dBm (9.5W). At this bias point, it consumes 42.4W DC power with a total DC current of 1.06A, and a DC voltage 40V.

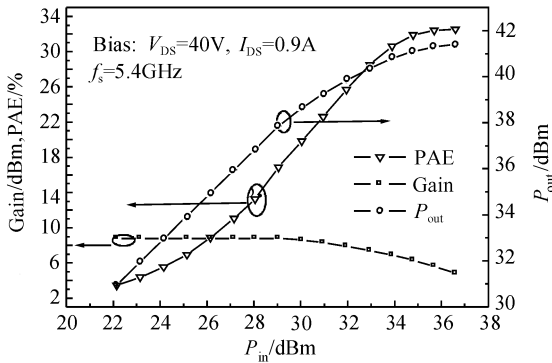


Fig. 5 CW power performance of the amplifier at 5.4GHz Bias point: $V_{DS} = 40V$, $I_{DS} = 0.9A$. $P_{outmax} = 41.4\text{dBm}$, $PAE_{max} = 32.54\%$ and $P_{1dB} = 39.8\text{dBm}$

For a $10 \times 120\mu\text{m}$ device at bias point $V_{DS} = 40V$, $I_{DS} = 0.26A$, the transistors are capable of delivering a maximum RF power of 37dBm at 5.4GHz. Thus we can obtain the power combine efficiency of about 69%.

The bandwidth of the amplifier under large signal conditions is also tested. We have a frequency sweep with a constant input power which made the output power near the 1dB gain compression. Figure 6 shows the bandwidth about 800MHz (5~5.8GHz) with a $\pm 0.5\text{dB}$ gain flatness.

5 Conclusion

Microwave integrated power amplifiers con-

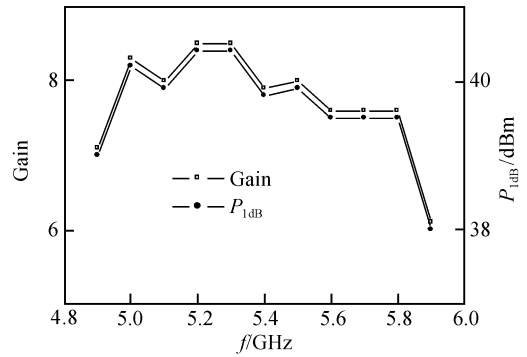


Fig. 6 Output power and gain versus frequency at fixed input power near 1dB gain compression Bias point: $V_{DS} = 40V$, $I_{DS} = 0.9A$

sisting of four parallel $10 \times 120\mu\text{m}$ AlGaIn/GaN HEMTs and Wilkinson combiner were designed, fabricated and measured. The highest CW output power at 5.4GHz was 41.4dBm with a maximum PAE of 32.54% and the power combine efficiency of 69%.

Acknowledgment The author would like to thank Professor Wang Xiaoliang from the Institute of Semiconductors of Chinese Academy of Sciences, for providing the GaN HEMTs epitaxial wafer.

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基于 AlGaIn/GaN HEMT 的 C 波段混合集成功率合成放大器的设计*

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摘要: 研制了一种基于 AlGaIn/GaN HEMT 的功率合成技术的混合集成放大器电路. 该电路包含 4 个 $10 \times 120 \mu\text{m}$ 的 HEMT 晶体管以及一个 Wilkinson 功率合成器和分配器. 在偏置条件为 $V_{\text{DS}} = 40\text{V}$, $I_{\text{DS}} = 0.9\text{A}$ 时, 输出连续波饱和功率在 5.4GHz 达到 41.4dBm, 最大的 PAE 为 32.54%, 并且功率合成效率达到 69%.

关键词: AlGaIn/GaN HEMT; 功率合成器; 混合集成电路; 微波功率放大器

EEACC: 1350F; 1350H

中图分类号: TN325⁺.3

文献标识码: A

文章编号: 0253-4177(2007)04-0514-04

* 国家重点基础研究发展计划(批准号:2002CB311903)及中国科学院重点创新工程(批准号:KGCX2-SW-107)资助项目

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2006-10-08 收到, 2006-10-27 定稿