A novel symmetrical microwave power sensor based on MEMS technology*

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Abstract: A novel symmetrical microwave power sensor based on MEMS technology is presented. In this power sensor, the left section inputs the microwave power, while the right section inputs the DC power. Because of its symmetrical structure, this power sensor provides more accurate microwave power measurement capability without mismatch uncertainty and temperature drift. The loss caused by the microwave signal is simulated in this power sensor. This power sensor is designed and fabricated using GaAs MMIC technology. And it is measured in the frequency range up to 20 GHz with an input power in the 0–80 mW range. Over the 80 mW dynamic range, the sensitivity can achieve about 0.2 mV/mW. The difference between the input power in the two sections is below 0.1% for an equal output voltage. In short, the key aspect of this power sensor is that the microwave power measurement is replaced with a DC power measurement.

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1. Introduction

Microwave power measurement is an important part of microwave wireless applications such as modern personal communication systems and radar systems. Thermocouplebased power sensors are the most widely used tool for microwave power measurements^[1]. The traditional power sensors based on thermocouples^[2] are subject to several error sources. Besides the error caused by the return loss resulting from a mismatch between the coplanar waveguide (CPW) and its load resistor, the thermal losses are main error sources, which include the heat conduction to the substrate, the convection loss and the radiation loss of the CPW, the load resistor and the thermopile. In order to reduce these thermal losses, it is of interest to remove the substrate directly underneath the thermal and microwave structures^[3]. To achieve this, a complex fabrication process is needed. Though thermal and electromagnetic losses are minimized by removing the bulk silicon located beneath the device through micromachining, the improvement is limited and the fabrication cost is increased.

In order to eliminate the error caused by the thermal losses and to simplify the fabrication process, a novel symmetrical microwave power sensor based on MEMS technology is presented, as shown in Fig. 1. The operation of the power sensor is based on the differential principle. The right section of this power sensor has the same CPW, load resistor and thermopile as the left section. So it eliminates the error caused by thermal losses, since the two sections have equal thermal losses. In this power sensor, the left section inputs the microwave power while the right section inputs the DC power. When the temperature of the left thermopile increases as a result of the left load resistor absorbing the microwave power, the temperature of the right thermopile is increasing simultaneously as a result of the right load resistor absorbing DC power from the voltage source E. The DC voltage source E is adjusted until the output voltages of the two sections are equal. In this way, the microwave power is done by a DC power measurement, which is achieved easily. To better equalize the temperature of the cold junctions to that of the substrate, the cold junctions are covered with a second-layer metal. Compared with the traditional power sensors based on thermocouples^[2, 3], the key aspect of this power sensor is that the microwave power measurement can be replaced with a DC power measurement.

2. Simulation

Compared with the DC source of the right section, the left section of the power sensor would produce additional losses which are caused by the microwave signal. The losses are mainly due to the transmission loss of the CPW and the parasitic loss of the load resistor. So it is necessary to analyze these two kinds of losses and to put forward a corresponding



Fig. 1. Novel symmetrical microwave power sensor.

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Table 1. Structural parameters.

Element	Parameter	Value
Center width of CPW	S	100 µm
Slot width	W	58 µm
Thickness of CPW	t	$2 \mu m$
Resistance	R	50 Ω
Characteristic impedance	Z_0	50 Ω
Effective permittivity	E.	12.9



Fig. 2. Loss coefficient versus microwave frequency.

loss model. The structural parameters of the loss model are the same as the structural parameters of the physical model, as shown in Table 1.

2.1. Loss model of the CPW

Due to electromagnetic coupling, the microwave signal will produce $losses^{[4]}$ when it moves through the CPW. The loss coefficient (α) can be expressed as

$$\Delta t = \frac{1.25t}{\pi} \left[1 + \ln \frac{4\pi s}{t} \right],\tag{1}$$

$$k_{\rm t} = \frac{s + \Delta t}{s + 2w - \Delta t},\tag{2}$$

$$b = 0.183(t + 0.464) - 0.095k_t^{2.484}(t - 2.595), \qquad (3)$$

$$a = \sqrt{\frac{\varepsilon_{\rm r} + 1}{2}} \left[\frac{45.152}{(sw)^{0.410} {\rm e}^{2.127 \sqrt{t}}} \right],\tag{4}$$

$$\alpha = af^b, \tag{5}$$

where *s* is the center width of the CPW, *w* is the slot width, *t* is the thickness of the CPW, *f* is the input microwave frequency, and ε_r is the substrate effective permittivity.

The relationship of the loss coefficient to the microwave frequency is shown in Fig. 2. The loss coefficient increases with microwave frequency, which shows that the power loss of the CPW also increases with microwave frequency.

2.2. Loss model of the load resistor

In the microwave frequency range^[5], the film load resistor does not act as a pure resistor but is subject to parasitic effects, which includes parasitic capacitances and inductivities. These capacitances and inductivities will store some power. The power is thus not completely dissipated in the film load



Fig. 4. Two kinds of power loss versus input power.

resistor. The parasitic equivalent circuit of the film load resistor in the microwave frequency range is shown in Fig. 3.

The circuit elements shown in Fig. 3 can be expressed as

$$R_{\rm S} = R \left(1 - \frac{\omega^2 \varepsilon_{\rm eff1} l_1^2}{3c^2} \right),\tag{6}$$

$$L_{\rm S} = \frac{Z_{01} \sqrt{\varepsilon_{\rm eff1}} l_1}{c} \left(1 - \frac{\omega^2 \varepsilon_{\rm eff1} l_1^2}{6c^2} + \frac{R^2}{6Z_{01}^2} \right), \tag{7}$$

$$C_{\rm p} = \frac{\sqrt{\varepsilon_{\rm eff1}} l_1}{2cZ_{01}} \left(1 + \frac{\omega^2 \varepsilon_{\rm eff1} l_1^2}{12c^2} \right),\tag{8}$$

$$R_{\rm p} = \frac{24c^2 Z_{01}^2}{\omega^2 \varepsilon_{\rm eff1} l_1^2 R},\tag{9}$$

where *R* is the low frequency resistance, Z_{01} is the characteristic impedance of the resistor in the lossless case, ε_{eff1} is the effective relative permittivity of the load resistor, and *c* is the velocity of light.

The losses in the CPW and in the load resistor vary with the input power at 10 GHz, as shown in Fig. 4. The parasitic loss in the load resistor is much lower than the loss in the CPW; so mainly the loss in the CPW is considered in this power sensor.

2.3. Simulation of loss

Considering the loss of the CPW, the power absorbed by the resistor is not equal to the input power (P_{in}). The relation between the loss coefficient (α) and the input power of the CPW (P_{in}) can be expressed as

$$\alpha = 10 \, \lg(P_{\rm in}/P_{\rm out}) \, (\rm dB/cm). \tag{10}$$

The output power of the CPW (P_{out}) is obtained by Eq. (11).

$$P_{\rm out} = P_{\rm in} / 10^{\frac{\alpha}{10}} \,({\rm W/cm}).$$
 (11)



Fig. 5. Loss power from simulation and calculation.

The loss coefficient (α) can be obtained by Eq. (5). So the loss power (P_{loss}) of the CPW is deduced as

$$P_{\text{loss}} = P_{\text{in}} - P_{\text{out}} = P_{\text{in}}(1 - 10^{-\frac{\alpha}{10}}). \tag{12}$$

The loss of the CPW is simulated at 10 GHz using the electromagnetic field solver HFSS from Ansoft Corporation and compared with the results from Eq. (12), as shown in Fig. 5. The difference between the results is below 4% for an input power below 100 mW.

3. Fabrication

The fabrication of this power sensor is compatible with the GaAs MMIC process^[6]. In this power sensor, the CPW line was designed to have a 50 Ω characteristic impedance. The thermopiles are made of Au and n⁺-GaAs with a Seebeck coefficient of 100 μ V/K. The Au was made by using a lift-off process through evaporating the 300/1800/300 Å Au/GeNi/Au layer, and n⁺-GaAs was made of a 2500 Å thick epitaxial layer. The load resistor was made by using a lift-off process through depositing a TaN layer with a square resistance of 25 Ω/\Box . Then a 500/1500/300 Å Ti/Au/Ti seed layer was sputtered and patterned. After removing the top Ti layer, the transmission lines of the CPW and the second-layer metal were formed through electroplating a 2 μ m thick Au layer. Finally, the GaAs substrate was thinned down to 100 μ m. In addition, since only a few, 80- μ m short thermopiles are used, the area of the chip is not larger than the area of traditional thermocouplebased power sensors. The process steps of the power sensor are shown in Fig. 6. The SEM photo of the power sensor is shown in Fig. 7.

4. Measurement

The power sensor was measured at different input powers and different microwave frequencies. First, in order to obtain an estimate of the input mismatch error of the power sensor, the return loss was measured. Second, in order to obtain the sensitivity and the relative deviation of input powers between the two sections, the power was measured.

4.1. Return loss measurement

The return loss of this power sensor was measured using a network analyzer and a Cascade Microtech 1200 probe





Fig. 7. SEM photo of the power sensor.

station. These were used to determine the input mismatch error over the entire frequency range (0-20 GHz) in the left section. The measurement results include the parasitic effects of the pad and underlying substrate. The return loss is below -26 dB over the entire frequency range, as shown in Fig. 8. This



Fig. 8. Measurement results of the return loss.



Fig. 9. Power measurement results before calibration.

reflects that the power sensor has a good matching characteristic.

4.2. Power measurement

The power measurement was accomplished with an Agilent E8257D signal generators, a Cascade Microtech 1200 probe station, a DC voltage source and two digital voltmeters.

For a power measurement^[7], the microwave signals are applied to the left section with a power in the range of 0 to 80 mW at 5, 10 and 15 GHz respectively, and the output voltage is recorded. In the right section, the DC voltage source E is adjusted to make the output voltage of the thermopiles of the two sections equal. The DC power is recorded as DC(1), DC(2) or DC(3) for 5, 10 or 15 GHz, respectively. The microwave power measurement results at different frequencies before calibration differ significantly from the corresponding DC power, as shown in Fig. 9. Under ideal conditions, the DC power curves corresponding to different frequencies would coincide. But the second metal is not ideal enough to keep the microwave power from influencing the DC power, so the curves DC(1), DC(2) and DC(3) have different slopes. However, the errors are negligible. In addition, due to the electromagnetic coupling loss of CPWs at microwave frequencies, the input microwave power results in unwanted frequency-dependent losses. As the loss power increases with microwave frequency, the microwave power absorbed by the load resistor decreases. Therefore, the output voltage decreases as a function of the microwave frequency and the DC supply can achieve the same output voltage with less power. So for a power



Fig. 10. Loss for different frequency levels.



Fig. 11. Power measurement results after calibration.

measurement, the zero correction losses of the microwave signal must be determined. The zero correction losses at different frequencies are measured by comparing measured DC power values with the DC power values at 0 dBm when the output voltage is equal. The measurement result is shown in Fig. 10. The zero correction losses at 5, 10 and 15 GHz are 1.56, 2.12 and 2.6 dBm, respectively. If the output voltage of the thermopiles of both sides is equal, the DC power is equal to the output power of the CPW (P_{out}). Therefore, the zero correction loss needs to be added to the DC power in order to determine the input power of the CPW (P_{in}) . The measurement results after calibration are shown in Fig. 11. At different frequencies, the relative deviation of the input power in two sections is below 0.1% at equal output voltages. The microwave power measurement can thus be replaced with a DC power measurement. The sensitivity is about 0.2 mV/mW with good linearity for the DC power measurement.

5. Conclusion

A novel symmetrical microwave power sensor based on MEMS technology is presented in this paper. The power sensor eliminates the error caused by the thermal losses and simplifies the fabrication process. The loss caused by the microwave signal is simulated. The measurement results show that this power sensor has good linearity and low return loss up to 20 GHz. The relative deviation of the input power in the two sections is below 0.1% with equal output voltage. So this power sensor is created to provide a more accurate microwave power measurement capability. However, the repeatability and reliability of this power sensor need to be studying in more detail in the future.

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