

High contrast ratio, high uniformity multiple quantum well spatial light modulators*

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Abstract: Our latest research results on GaAs–AlGaAs multiple quantum well spatial light modulators are presented. The thickness uniformity of the epitaxial layers across the 3-inch wafer grown by our molecular beam epitaxy is better than 0.1% and the variation of cavity resonance wavelength within the wafer is only 0.9 nm. A contrast ratio (CR) of 102 by varying bias voltage from 0 to 6.7 V is achieved after fine tuning the cavity by etching an adjust layer. Both theoretical and experimental results demonstrate that incorporating an adjust layer is an effective tuning method for obtaining high CR.

Key words: spatial light modulator; multiple quantum well; uniformity; contrast ratio; adjust layer; matching condition
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1. Introduction

The quantum-confined Stark effect (QCSE), reported by Miller *et al.* in 1984^[1], had been used in multiple quantum well (MQW) electroabsorption modulators in the late 1980s. Later, the Fabry–Perot (F–P) cavity was introduced^[2,3], which made high contrast ratio (CR), low drive voltage and low insertion loss modulators realizable. Nowadays, the spatial light modulator (SLM) has become a key component in many electro-optic applications, for example, optical computation^[4], free-space optical communication^[5], optical correlation^[6] and laser beam steering^[7]. High CR, low driving voltage, low insertion loss SLMs have been reported^[8]. To our knowledge, the best CR and uniformity reported in mainland of China are about 10 : 1^[9] and 2.2%^[10]. In our latest research development, uniformity of better than 0.1% and CR of 102 are achieved.

The most common structure of MQW SLM is to embed i-type MQW absorption area between p- and n-type distributed Bragg reflector (DBR) mirrors, which form an asymmetric F–P cavity with absorption^[3]. In this structure, MQW introduces enhanced absorption peak at the heavy hole exciton resonance wavelength (λ_{EX}), while the F–P cavity provides resonance reflection at a certain wavelength (λ_{FP}), which is also the working wavelength of the device. λ_{FP} is usually placed at the longer wavelength side of λ_{EX} . The operation principle is to move λ_{EX} toward λ_{FP} by applying an electric field (according to QCSE) in order to lead a change in both absorption and reflectivity at λ_{FP} .

The performance of the modulator is primarily characterized by its CR, the ratio between the amount of light reflected in the ON state (R_{ON}) and in the OFF state (R_{OFF}). Near-zero

R_{OFF} is important for a high CR. A matching condition (balance condition) which must be satisfied to achieve a near-zero R_{OFF} ^[11] is as follows:

$$R_f = R_b \exp(-2\alpha L) = R_{\text{beff}}, \quad (1)$$

where R_f , R_b is the reflectivity of front and back mirror, α is the absorption coefficient in MQW, L is the length of F–P cavity, and R_{beff} is the effective reflectivity of back mirror.

2. Material design, growth and device fabrication

Computer modeling and calculation are used for material design^[12,13]. After that, the multi-layer structure (labeled V0726) is grown in a V90 molecular beam epitaxy (MBE) system on a GaAs wafer. The schematic diagram of the material is shown in Fig. 1. In the direction from the top epitaxial layer to the substrate, there are 1000 Å $\text{Al}_{0.2}\text{Ga}_{0.8}\text{As}$ adjust layer, 5 periods of n-type front DBR, 28 periods of 72 Å/45 Å $\text{GaAs}/\text{Al}_{0.35}\text{Ga}_{0.65}\text{As}$ MQW, n-contact GaAs layer, 25 periods of p-type bottom DBR, and etching stop layer. Because the only one Al source in the MBE system is calibrated for $\text{Al}_{0.5}\text{Ga}_{0.5}\text{As}$ (not $\text{Al}_{0.1}\text{Ga}_{0.9}\text{As}$) at the time of this growth, short period superlattice (GaAs (40 Å)/ $\text{Al}_{0.5}\text{Ga}_{0.5}\text{As}$ (10 Å)) is used to serve as a layer of $\text{Al}_{0.1}\text{Ga}_{0.9}\text{As}$ to obtain a high accuracy of growth rate and Al concentration. The wafer is rotated during the MBE growth to ensure a good uniformity. λ_{EX} and λ_{FP} are designed at 840 nm and 860 nm, respectively.

Single pixel devices were fabricated by a three-step process: top ring p-contact deposition and lift-off, mesa wet-etching and bottom n-contact deposition and lift-off. Mesa devices were then packaged and wire-bonded for measurement.

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	Al _{0.2} Ga _{0.8} As	1000 Å	Be	8e18	Adjust layer
	AlAs	720.6 Å	Be	1e19	
5×	GaAs	40 Å	Be	1e19	Front DBR
	Al _{0.5} Ga _{0.5} As	10 Å	Be	5e18	
	Al _{0.35} Ga _{0.65} As	90 Å			
28×	GaAs	72 Å			Active area
	Al _{0.35} Ga _{0.65} As	50 Å			
	GaAs	72 Å			
	Al _{0.35} Ga _{0.65} As	90 Å			
71×	GaAs	40 Å	Si	3e18	N contact
	Al _{0.5} Ga _{0.5} As	10 Å	Si	1.5e18	
	GaAs	40 Å	Si	3e18	
25×	Al _{0.5} Ga _{0.5} As	10 Å	Si	1.5e18	Bottom DBR
	AlAs	720.6 Å	Si	3e18	
	AlAs	1500 Å	Si	3e18	
	GaAs	3000 Å			
	GaAs	n+			Etching-stop
	GaAs	n+			Buffer layer
	GaAs	n+			Substrate

Fig. 1. Multi-layer structure of V0726.

The mesa size of the device is from $400 \times 400 \mu\text{m}^2$ to $4000 \times 4000 \mu\text{m}^2$.

3. Experiment results

First, we measured the reflectivity spectra of the material to evaluate its optical properties using a Fourier transform infrared spectroscopy (FTIR) system. The light source is collimated and focused to 0.5 mm diameter. Only the center of the reflected beam is collected to ensure good direction consistency. The test results show λ_{EX} is at 836 nm, while λ_{FP} is at 871 nm. The deviation between designed and measured values is caused by a slightly larger than intended MBE growth rate. This growth rate error can be estimated by the deviation of λ_{FP} : $(871 - 860)/860 = +1.3\%$. Indeed, an X-ray diffraction (XRD) analysis shows that the layer thickness is about 1% larger than the designed value. If we re-model the device performance with the new parameters extracted from XRD, λ_{FP} should be at 873 nm, very close to the experimental data. This confirms the validity of our modeling and design. Greater accuracy for layer thickness, thus even more accurate mode location, can be achieved with MBE reproducibly, but only by employing *in-situ* growth rate monitoring coupled real-time to the recipe execution.

The lateral uniformity is important for the development of a high volume 2-D SLM array. We picked 11 points across the whole wafer to evaluate the uniformity. The λ_{FP} position mapping is shown in Fig. 2. The maximum variation of λ_{FP} across the wafer diameter is 0.9 nm. The lateral uniformity of the device structure is within $0.9/871 = 0.1\%$ ^[14]. This has been achieved by optimizing the geometry of the MBE system and of the molecular beams. Previous reports on this value is 0.7% across a 4-inch wafer^[15]. To our knowledge, our result is the best for this kind of device to date.

Under normal incidence conditions, the measured wavelength spacing between λ_{EX} and λ_{FP} is 35 nm, which is too large to get a high absorption change for a high CR. (The proper spacing between λ_{EX} and λ_{FP} is about 10–15 nm.). In this case, a very high drive voltage is required. To solve this problem, an oblique incident angle is utilized to decrease the spacing. Under the 40-degree incident angle condition, λ_{FP} is shifted from 871

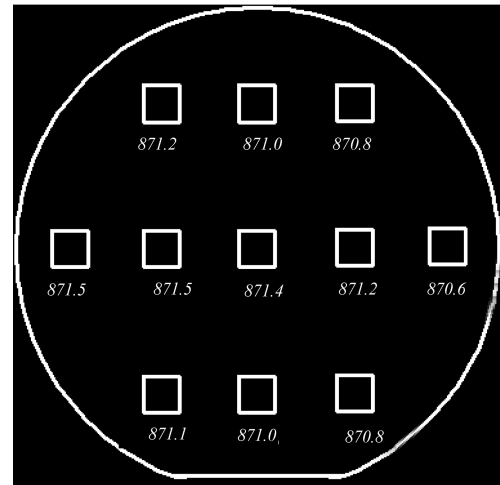


Fig. 2. Measured F-P mode (λ_{FP}) position across the whole wafer (nm). Maximum variation of λ_{FP} is only 0.9 nm.

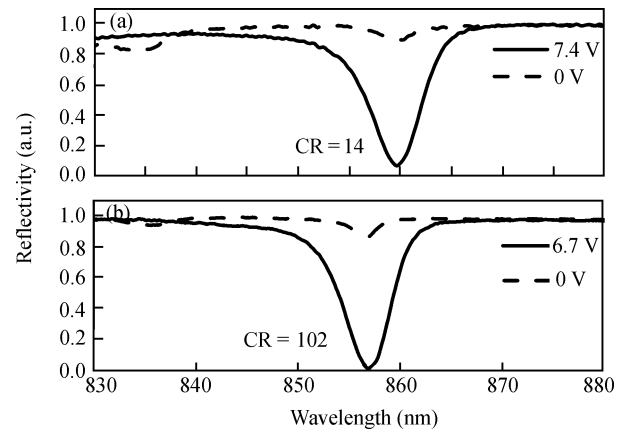


Fig. 3. Measured reflectivity spectra (a) before and (b) after 400-Å adjust layer etching.

to 860 nm and CR of 14 is achieved. However, even in such a large incident angle, there is still a 24 nm spacing between λ_{EX} and λ_{FP} . When λ_{EX} is shifted into λ_{FP} by such a long distance, the exciton absorption at this point will be too weak to fulfill the matching condition. A top adjust layer is adopted to overcome the problem. (The principles of adjust layer will be discussed in the next section.) The 400-Å-thick adjust layer on the top is wet-etched and the reflectivity spectra are re-measured. After the adjustment, λ_{FP} is shifted by 4 nm to the shorter wavelength side and the near-zero reflectivity is achieved at 6.7 V with CR = 102. The reflectivity spectra before and after adjust layer etching are shown in Fig. 3.

4. Discussion

In the design and fabrication process of MQW SLMs, the allocation of λ_{EX} and λ_{FP} is very important. Their positions determine the working wavelength, while the spacing between them dictates the modulation depth and CR value. However, λ_{FP} is very sensitive to the layer thickness of the epitaxial material. Generally, a small deviation of the experimental result from the designed value exists, unless the growth rate is strictly calibrated and *in-situ* correction is introduced during

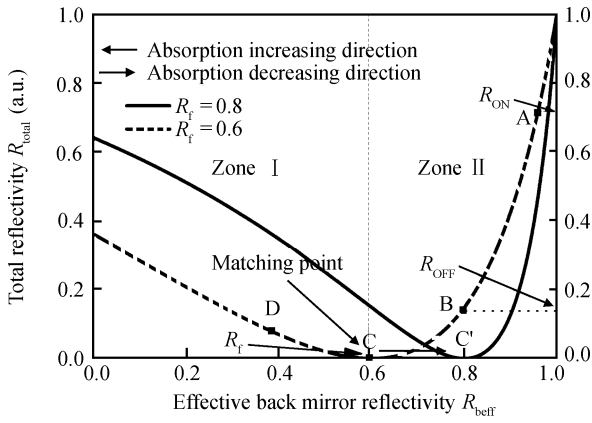


Fig. 4. Total reflectivity versus effective bottom mirror reflectivity with given $R_f = 0.6$ and 0.8 . R_{beff} is inversely proportional to α , so from left to right (R_{beff} increasing) means α decreasing, from right to left means α increasing.

the growth process^[16]. When the spacing between λ_{EX} and λ_{FP} is larger or smaller than the designed value, the absorption coefficient at working wavelength can be changed substantially because of the intensity change of QCSE effect under applied electric field. Adjust layer etching is a practical way to solve the problem. This method has been reported before^[17], but unlike the previous paper, here we will focus on the effect of the matching condition tuning.

Equation (2) describes the relationship between the device total reflectivity (R_{total}) and R_{beff} for a given R_f at λ_{FP} ^[18]:

$$R_{\text{total}} = \left| \frac{R_f - R_{\text{beff}}}{1 - R_f R_{\text{beff}}} \right|^2 \quad (2)$$

The calculated relationship is shown in Fig. 4. See dashed line curve for $R_f = 0.6$. Under zero electrical field, the absorption at F–P mode is small and R_{beff} is large. At this moment the device works at zone II, hypothetically at point A. With a small biased voltage, the absorption coefficient increases, making R_{beff} decrease. Thus, the working point moves left along the curve to B. When R_{beff} decreases to R_f , the working point shifts to point C and a near-zero R_{OFF} is achieved. Point C, whose horizontal coordinate equals to R_f , is usually called the matching point. However, in our experiment, the spacing between λ_{EX} and λ_{FP} is large and the absorption at λ_{FP} is very small with biased voltage. In Fig. 4, it means the working point is just near B, unable to reach the matching point C. If we can find a method to adjust the position of point C (as well as R_f), it is possible to satisfy the matching condition.

As an additional layer above top DBR, the 1000-Å $\text{Al}_{0.2}\text{Ga}_{0.8}\text{As}$ adjust layer is used to make a change of R_f . We calculate R_f versus adjust layer thickness using transform matrix method^[19], which is shown in Fig. 5. By etching the adjust layer, R_f increases from 60% to 80%, then decreases to 10%. In Fig. 4, this means we can adjust the matching point (C) position in a very wide range. If the adjust layer is etched by about 400 Å, R_f will increase from 60% to 80% and the solid curve will become the new working line. The working point of device will be changed from B to C', so a lower low-state reflectivity can be realized. Moreover, an etching of the adjust layer will shift the F–P mode to the short-wavelength side, making the

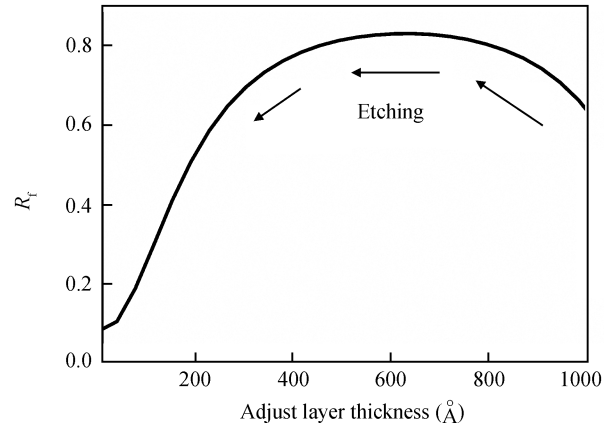


Fig. 5. Calculated R_f versus adjust layer thickness.

absorption at F–P position larger and the matching condition easier to satisfy. Experimental data (Fig. 3) shows clearly that by such an adjustment method, R_{OFF} decreases from 6.3% to 0.8% and CR increases from 14 to 102, and λ_{FP} is blue-shifted by about 4 nm, which is in accordance with the theory^[17].

5. Conclusion

We have presented our latest research results on MQW SLMs in this paper. The material is grown by MBE. A spread of only 0.9 nm of λ_{FP} is achieved across the 3-inch-diameter wafer, representing a lateral uniformity of better than 0.1%. The single pixel devices are fabricated and the reflectivity spectra are measured. When the incident angle is 40 degree, CR reaches 14 for the applied bias voltage of 7.4 V. After etching the adjust layer which is on the top of the device to tune the matching condition, CR is successfully increased to 102 at 6.7 V voltage. This work is an indispensable base toward the fabrication of 2-D arrays of GaAs/AlGaAs MQW SLM bonded to CMOS driving circuit in the following research.

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