

## Call for Papers (Special Issue on Reconfigurable Computing for Energy Efficient AI Microchip Technologies)

### CALL FOR PAPERS

Many Artificial Intelligence (AI) processing tasks, especially those related to deep neural networks, are both computation and memory intensive. Yet the traditional computing platforms such as CPU are increasingly facing difficulties in dealing with those massive workloads. Reconfigurable Computing (RC) features the ability to perform computations in hardware to increase execution capabilities, and at the same time retain much of the flexibility of a software solution. The microchip design based on the Reconfigurable Computing models and principles has emerged as an effective means to ensure that the AI applications can be accelerated to not only meet the performance and throughput targets but also power and energy efficiency requirements.

In this special issue of the Journal of Semiconductors, we call for the most advanced research results on various AI microchip design technologies based on the Reconfigurable Computing architectures. We invite submissions of original research articles/communications and comprehensive reviews to this special issue. Topics of interest include (but are not limited to) the following:

- Software/Compilers/Tools for mapping deep neural networks to RC-based accelerators
- Micro-architecture designs of hardware accelerators for RC
- Deep neural network acceleration on existing accelerators such as FPGA, CGRA, or PSoC
- RC-based acceleration for edge computing and IoT; RC-based acceleration for cloud computing
- Hardware friendly deep neural network modeling, optimization, quantization, and compression Comparison studies of different RC-based acceleration architectures (FPGAs, CGRAs, PSoCs, etc.)
- Reconfigurable approaches applied to analog or mixed signal integrated circuits and signal processing
- Reconfigurable design for intelligent MEMS sensory systems
- Survey and tutorial studies of AI microchip design technologies based on the Reconfigurable Computing architectures.

Prof. Shaojun Wei of Tsinghua University will preface this special issue in the Comments and Opinions.

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### **Key timetable dates:**

Manuscript due: **August 31, 2019**

Authors' notification: **September 30, 2019**

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