Supporting information for

Multilayered PdTe₂/Thin Si Heterostructures as Self-powered Flexible

Photodetectors with Heart Rate Monitoring Ability

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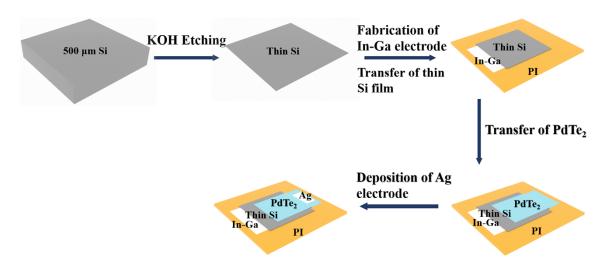


Figure S1. Schematic diagram of the procedures for fabricating PdTe₂ multilayer/thin Si heterostructure-based flexible photodetector.

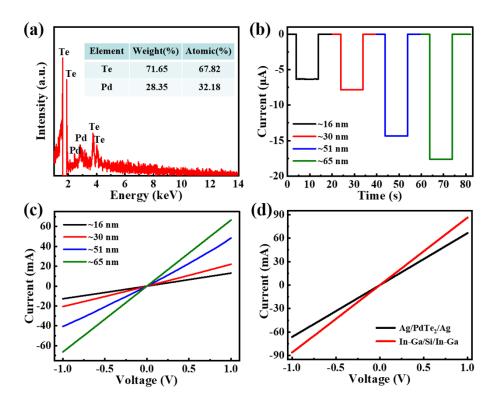


Figure S2. (a) EDS spectrum of the PdTe₂ sample. The inset shows the weight and atomic ratios of Pd and Te elements. (b) Transient photoresponse of the heterostructure devices with different PdTe₂ thicknesses. (c) *I-V* curves of Ag/PdTe₂/Ag structures with different PdTe₂ thicknesses. (d) *I-V* curves of Ag/PdTe₂/Ag and In-Ga/Si/In-Ga structures, showing good ohmic contacts between PdTe₂ and Ag, as well as In-Ga and n-Si.

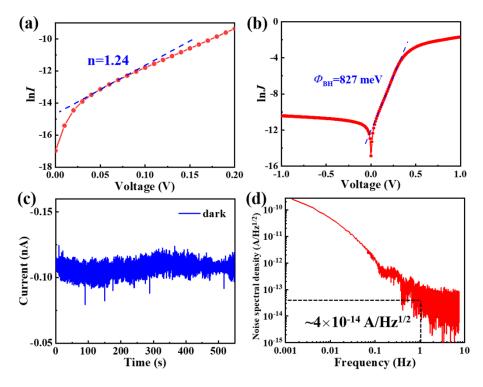


Figure S3. (a) $\ln I - V$ curve of the heterostructure for estimating the diode ideality factor (*n*). (b) The plot of $\ln J - V$ curve for calculating the barrier height of the heterostructure. (c) The noise of the dark current of the light detector at zero bias. (d) The noise spectral density based on the Fourier transform of the dark current.