

AlGaN/GaN High Electron Mobility Transistors on Sapphires with f_{\max} of 100 GHz *

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Abstract: AlGaN/GaN high electron mobility transistors grown on sapphire substrates with a 0.3 μm gate length and 100 μm gate width are fabricated. The device reveals a drain current saturation density of 0.85 A/mm at a gate voltage of 0V and a peak transconductance of 225 mS/mm. The unity current gain cutoff frequency and maximum frequency of oscillation are obtained as 45 and 100 GHz, respectively. The output power density and gain are 1.8 W/mm and 9.5 dB at 4 GHz, and 1.12 W/mm and 11.5 dB at 8 GHz.

Key words: AlGaN/GaN; HEMT; sapphire

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1 Introduction

GaN-based high electron mobility transistors (HEMTs) are one of the most promising candidates for high power, high speed, and high temperature applications due to the inherent advantages of the material. These advantages include high breakdown voltage, high carrier velocity, and very high two-dimensional electron gas (2DEG) concentration resulting from strong polarization and piezo effects with a heterojunction structure^[1,2]. With the great development in the epitaxial growth and fabricating process, excellent frequency characteristics and microwave power performances of AlGaN/GaN HEMTs have been reported^[1~7]. Output power densities of 6.4 W/mm and above 10 W/mm in the X-band have been measured on AlGaN/GaN HEMTs grown on sapphire and SiC, respectively^[1,4]. As well as being the best component for X band high power microwave applications, GaN

HEMTs are attractive for the application of high temperature circuits at even higher frequencies. A current gain cutoff frequency f_T of 121 GHz and a maximum oscillation frequency f_{\max} of 162 GHz for AlGaN/GaN HEMTs have been reported^[7].

In this paper, we report a high frequency AlGaN/GaN HEMT grown by MOCVD on sapphire with a f_T of 45 GHz and a f_{\max} of 100 GHz fabricated in our in-house 0.3 μm T-type gate process.

2 Device structure and fabricating process

The epitaxial structure of the AlGaN/GaN HEMTs was grown by the metal organic chemical vapor deposition (MOCVD) system on a 50 mm commercially available sapphire substrate. A 20 nm thick nucleation AlN layer was grown on the sapphire, and followed by a 2 μm undoped GaN buffer layer and a 3 nm undoped Al_{0.3}Ga_{0.7}N space layer; a 15 nm Si-doped Al_{0.3}Ga_{0.7}N layer with Si dopant

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density of $5 \times 10^{18} \text{cm}^{-3}$ was then grown to form the 2DEG and capped by a 2nm undoped $\text{Al}_{0.3}\text{Ga}_{0.7}\text{N}$ Schottky barrier layer. Hall measurement at room temperature reveals a low field Hall mobility of $980 \text{cm}^2/(\text{V} \cdot \text{s})$, a sheet resistance of $383 \Omega \cdot \text{cm}^{-2}$ and a sheet carrier density of $1.66 \times 10^{13} \text{cm}^{-2}$ in the 2DEG formed at the $\text{AlGaIn}/\text{GaIn}$ interface; at 77K, a low field mobility of $2820 \text{cm}^2/(\text{V} \cdot \text{s})$, a sheet resistance of $125 \Omega \cdot \text{cm}^{-2}$, a sheet carrier density of $1.77 \times 10^{13} \text{cm}^{-2}$ were recorded.

The fabricating process started with mesa isolation-removing the $\text{AlGaIn}/\text{GaIn}$ of about 100nm outside the active mesa using a BCl_3/Ar based inductively coupled plasma (ICP) dry etching process. To enhance the isolation effect, He^+ implantation was added when the quality of the GaN buffer layer was poor. Ti/Al/Ni/Au was deposited through E-beam evaporation followed by lift-off and annealing at 890 °C for 30s to form the ohmic contacts for source and drain. A specific contact resistance of $0.88 \text{m}\Omega \cdot \text{mm}$ was measured through the TLM (transmission line method) pattern. The Schottky gate was patterned using E-beam lithography and metalized with Ni/Au by evaporation and lift-off. The gate with length and width of $0.3\mu\text{m}$ and $100\mu\text{m}$, respectively, was offset to be closer to the source contact in the $3\mu\text{m}$ source-drain space. An 80nm thick SiN_x was then deposited by PECVD for surface passivation to suppress the effect of current collapse. Finally, interconnecting metal and air-bridge structure were achieved with electrical plating Au of about $2\mu\text{m}$ thickness.

3 DC and microwave performances

The DC current-voltage and transfer characteristics of the fabricated $100\mu\text{m}$ -gate-width device were measured on wafer with the Keithely 4200 semiconductor parameter analyzer as shown in Figs. 1 and 2. The saturation drain current density was $0.85 \text{A}/\text{mm}$ at a gate voltage of 0V and the threshold voltage was typically -5V. The peak transconductance was $225 \text{mS}/\text{mm}$. A self-heating

effect at large current was observed from the I - V characteristic in Fig. 2, which was due to the poor thermal conductivity of the sapphire substrate.

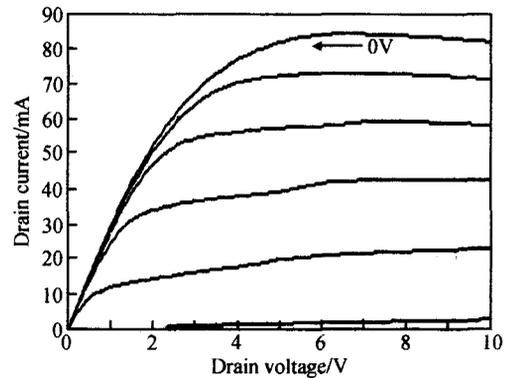


Fig. 1 Drain current-voltage characteristics of AlGaIn/GaN HEMT with $0.3\mu\text{m} \times 100\mu\text{m}$ gate. Gate voltage: -1V/step

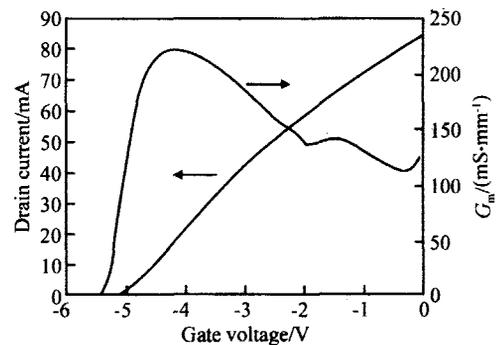


Fig. 2 Transfer characteristics of AlGaIn/GaN HEMT with $0.3\mu\text{m} \times 100\mu\text{m}$ gate

The microwave characteristics were measured on wafer by using an Agilent 8722ES network analyzer from 1 to 40 GHz. Both short-circuit current gain h_{21} and unilateral power gain U were calculated from the measured S -parameters and extrapolated at -20dB/decade to find f_T and f_{max} , respectively. As shown in Fig. 3, the f_T and f_{max} were measured to be 45 and 100 GHz, respectively. Furthermore, microwave output power performances were evaluated on wafer using MT 986 load-pull measurements at different frequencies. Figures 4 and 5 reveal output power and gain versus input power for the above devices at 4 and 8 GHz. The continuous wave (CW) output power performance was limited by three reasons. The first reason is

that the maximum input power of the measurement system was 8dBm. If the input power is larger ,the output power can then be higher. Another reason is that the breakdown voltage of source and gain was only about 13V and limited the power performance seriously. This was believed to be a result of the lower quality of the GaN buffer epitaxial layer. Finally ,the poor thermal conductivity of the sapphire substrate also affected the power characteristics.

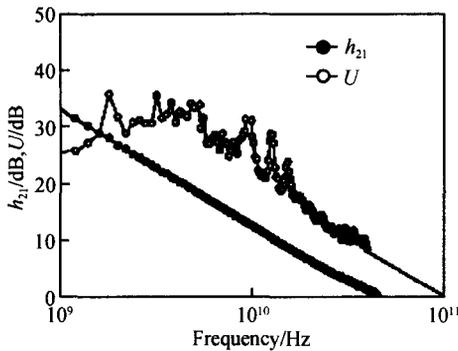


Fig. 3 Short-circuit current gain h_{21} and unilateral power gain U versus frequency

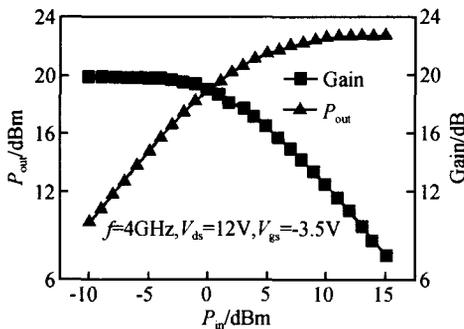


Fig. 4 Microwave output power performances of Al-GaN/ GaN HEMT with $0.3\mu\text{m} \times 100\mu\text{m}$ gate at 4GHz

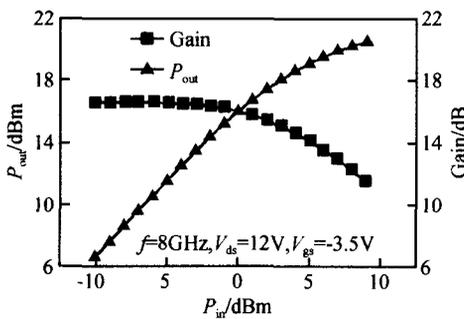


Fig. 5 Microwave output power performances of Al-GaN/ GaN HEMT with $0.3\mu\text{m} \times 100\mu\text{m}$ gate at 8GHz

4 Conclusion

AlGaIn/ GaN HEMTs grown by MOCVD on sapphire with a gate length and width of 0.3 and $100\mu\text{m}$, respectively, were demonstrated. The saturation drain current density was 0.85A/mm at a gate voltage of 0V and the threshold voltage was typically -5V. The peak transconductance was 225mS/mm. The f_T and f_{max} were measured as 45 and 100 GHz, respectively. The output power density and the gain were 1.8W/mm and 9.5dB at 4GHz, and 1.12W/mm and 11.5dB at 8GHz.

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f_{\max} 为 100 GHz 的蓝宝石衬底 AlGaIn/GaN 高电子迁移率晶体管*

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摘要: 制作了蓝宝石衬底上生长的 AlGaIn/GaN 高电子迁移率晶体管. 0V 栅压下, 0.3 μ m 栅长、100 μ m 栅宽的器件的饱和漏电流密度为 0.85 A/mm, 峰值跨导为 225 mS/mm; 特征频率和最高振荡频率分别为 45 和 100 GHz; 4 GHz 频率下输出功率密度和增益分别为 1.8 W/mm 和 9.5 dB, 8 GHz 频率下输出功率密度和增益分别为 1.12 W/mm 和 11.5 dB.

关键词: AlGaIn/GaN; HEMT; 蓝宝石

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